SLM757 8GB(16GB) LTE
TD-LTE/FDD-LTE/WCDMA/EVDO/TD-SCDMA/CDMA/GSM
SLM757 Android6.0

TD-LTE: 117/30Mbps
FDD-LTE: 150Mbps/50Mbps
WCDMA DC HSPA+: 42Mbps/5.76Mbps
EVDO EVDO RevA: 3.1Mbps/1.8Mbps
TD-SCDMA HSPA: 4.2Mbps/2.2Mbps
CDMA1x: 153.6kbps/153.6kbps
GSM EDGE: 236.8kbps/236.8kbps

SLM757 WiFi BT GPS

	Qualcomm MSM8917
CPU	Quad-core A53 (64bit) 1.4GHz
GPU	A308 600MHz
	8GB eMMC + 1GB LPDDR3 16GB+2GB
	Android6.0
	41.0x41.0x3.0mm 146pin+92pin LGA

SLM753-MG02
8916-5

	TD-LTE	Cat4 TD-LTE 117/30Mbps
	FDD-LTE	Cat4 FDD-LTE 150/50Mbps
	DC-HSPA+	42/11.2Mbps
	TD-HSPA	2.8/2.3Mbps
	EVDO Rev.A	3.1/1.8Mbps
	EDGE	Class12, 236.8kbps/236.8kbps
	GPRS	Class12, 85.6kbps/85.6kbps
SIM		DSDA 3.0/1.8V SIM L/W/G+G with CSFB to W/G L/TDS/G+G with CSFB to TDS/G L/EVDO/CDMA1x+CDMA1x
Display		Matrix: HD 1280*800 60fps; FULL HD: 1920*1200 45fps
		LCM Size: User defined
		Interface: MIPI DSI 4-lane
Camera		Interface: main: MIPI CSI 4-lane; front: MIPI CSI 4-lane LCM Size

	PIN	LTE LTE GPS 2.4G WiFi-
	Audio	1 MIC 1 MIC 1 0.8W D 1 1

SLM757-QE	TDD-LTE: B38/40/41 FDD-LTE: B1/3/5/7/8/20 WCDMA: B1/B5/B8 GSM: B5/3/8
SLM757-QA	FDD-LTE B2/4/5/12/13/17 WCDMA B2/4/5/8

PIN#	SLM757 Pin name	Pad characteristics	Functional description
1	GND	GND	GND
2	VBUS	PI,PO	USB Voltage

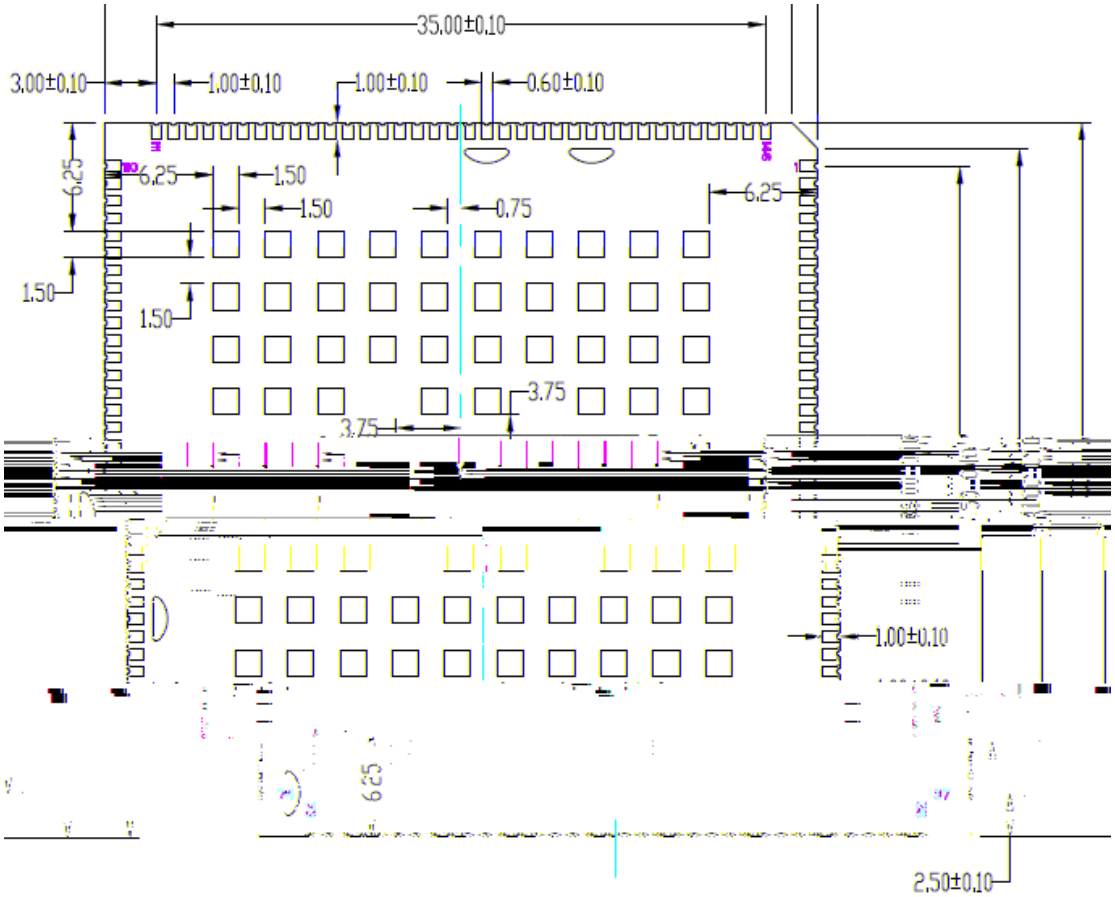
66	MIPI_DSI0_CLK_P	AO	MIPI display serial interface 0 clock+
67	MIPI_DSI0_LANE1_N	AO	MIPI display serial interface 0 lane1-
68	MIPI_DSI0_LANE1_P	AO	MIPI display serial interface 0 lane1+
69	MIPI_DSI0_LANE3_N	AO	MIPI display serial interface 0 lane3-
70	MIPI_DSI0_LANE3_P	AO	MIPI display serial interface 0 lane3+
71	MIPI_DSI0_LANE2_N	AO	MIPI display serial interface 0 lane2-
72	MIPI_DSI0_LANE2_P	AO	MIPI display serial interface 0 lane2+
73	GND	GND	GND
74	RF_WIFI/BT	AI	RF signal for WIFI/BT
75	GND	GND	GND
76	GPIO5_UART0_RX	B-PD:nppukp	Configurable I/O,UART0 RX
77	GPIO4_UART0_TX	B-PD:nppukp	Configurable I/O,UART0 TX
78	GND	GND	GND
79	GPIO65_TP_INT_N	B-PD:nppukp	Configurable I/O,TP INT
80	GPIO64_TP_RST_N	B-PD:nppukp	Configurable I/O,TP RESET
81	GPIO11_TP_I2C3_SCL	B-PD:nppukp	Configurable I/O,TP I2C SCL
82	GPIO10_TP_I2C3_SDA	B-PD:nppukp	Configurable I/O,TP I2C SDA
83	GND	GND	GND
84	RF_5G	AI	RF signal for WIFI 5G
85	GND	GND	GND
86	GPIO61_LCD_ID	B-PD:nppukp	Configurable I/O,primary LCD ID
87	GPIO60_LCD_RST_N	B-PD:nppukp	Configurable I/O,primary LCD RESET
88	GPIO24_LCD_TE0	B-PD:nppukp	Configurable I/O,primary LCD TE
89	GND	GND	GND
90	RF_MAIN	AI	RF signal for main ANT
91	GND	GND	GND
92	GPIO15_SENSOR_I2C4_SCL	B-PD:nppukp	Configurable I/O,SENSOR I2C SCL
93	GPIO14_SENSOR_I2C4_SDA	B-PD:nppukp	Configurable I/O,SENSOR I2C SDA
94	GPIO7_I2C2_SCL	B-PD:nppukp	Configurable I/O,I2C SCL or UART0 RTS
95	GPIO6_I2C2_SDA	B-PD:nppukp	Configurable I/O,I2C SDA or UART0 CTS
96	UIM2_DET	B-PD:nppukp	Configurable I/O,UIM2 removal detection
97	UIM2_RESET	B-PD:nppukp	Configurable I/O,UIM2 reset
98	UIM2_CLK	B-PD:nppukp	Configurable I/O,UIM2 clock
99	UIM2_DATA	B-PD:nppukp	Configurable I/O,UIM2 data
100	VREG_L15_UIM2	PO	PMIC output for UIM2
101	UIM1_DET	B-PD:nppukp	Configurable I/O,UIM1 removal detection

102	UIM1_RESET	B-PD:nppukp	Configurable I/O,UIM1 reset
103	UIM1_CLK	B-PD:nppukp	Configurable I/O,UIM1 clock
104	UIM1_DATA	B-PD:nppukp	Configurable I/O,UIM1 data
105	VREG_L14_UIM1	PO	PMIC output for UIM1

	GN
	VE
	VE
	V
6	G
47	G
148	G
149	L

n[(71TE]

2D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1.00±0.10
0.60±0.10
6.25
6.25
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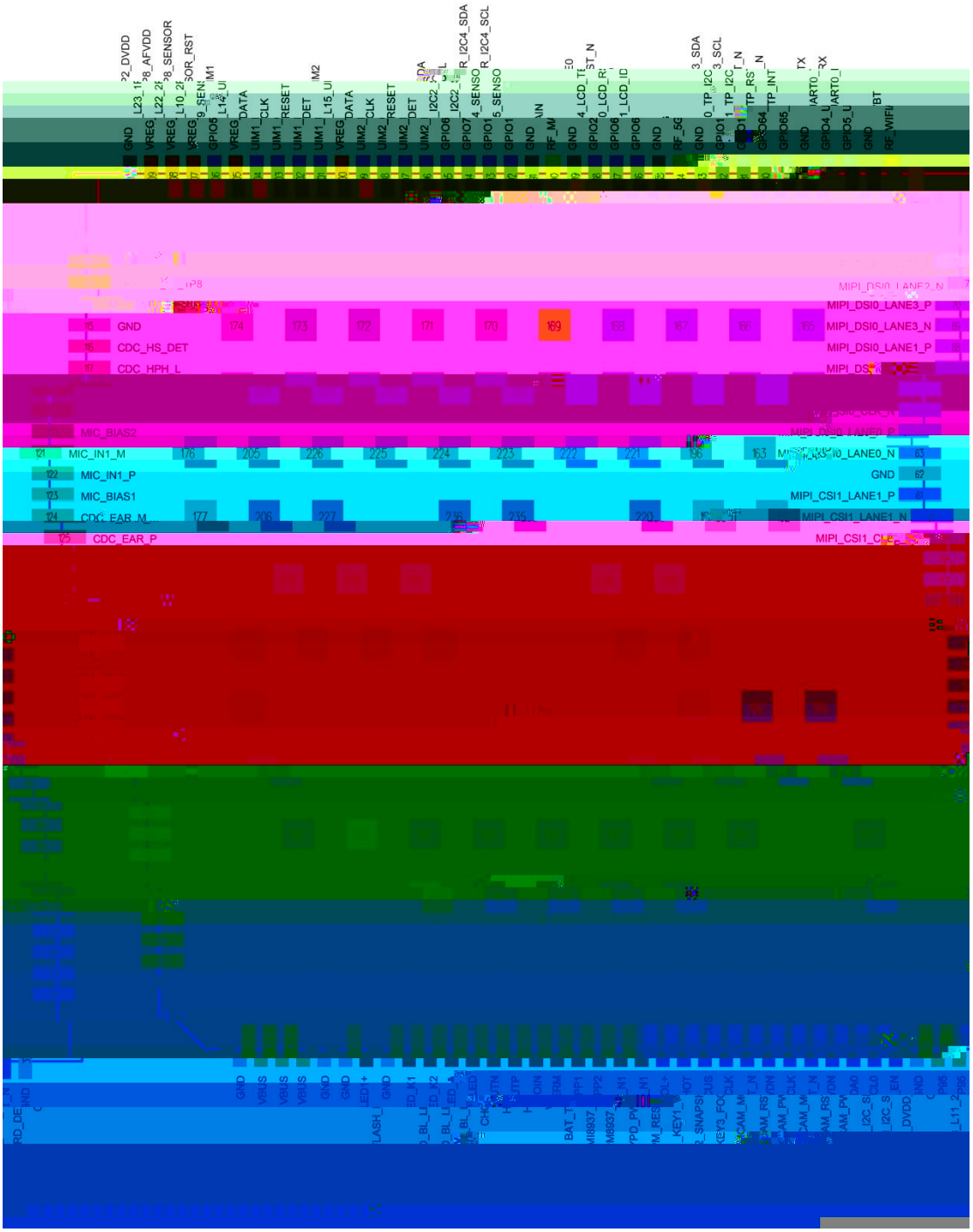
6.25
1.50
1.50
3.75
3.75
1.00±0.10
6.25
2.50±0.10


GND
CDC_HS_DET
CDC_HPH_L
MIC_BIAS2
MIC_IN1_M
MIC_IN1_P
MIC_BIAS1
CDC_EAR_M
CDC_EAR_P
MIPI_DSI0_LANE3_P
MIPI_DSI0_LANE3_N
MIPI_DSI0_LANE1_P
MIPI_DSI0_LANE0_N
GND
MIPI_CSI1_LANE1_P
MIPI_CSI1_LANE1_N